



2023 CCF CHINASOFT

中国软件大会

智能化软件创新推动数字经济与社会发展

NASAC

第22届全国软件与应用学术会议

The 22nd National Software and Application Conference

FMAC

第8届全国形式化方法与应用会议

The 8th National Conference on Formal Method and Application

中国·上海 2023年12月1-3日
SHANGHAI·CHINA December 1-3, 2023





2023 CCF CHINASOFT
中国软件大会

软件系统性能优化的教学与实践

报告人：郭健美

报告日期：2023.12.1



- **教学I：软件系统优化**
- **教学II：计算机系统**
- **实践I：跨平台的硬件性能计数器自适应分组复用 [TACO' 23]**
- **实践II：不同编译器优化能力的分析与识别 [CC' 23]**

为何开设“软件系统优化”课？



- 性能是衡量软件系统质量和竞争力的一个重要方面，是软件系统设计、开发和应用过程中必须关注的一个基本属性。如何在给定的硬件资源配置下提升软件系统的性能，是数字化系统的设计和实现必须思考和解决的问题，同时也是优化利用软硬件资源的有效途径。
- 软件系统优化的原理、技术和实践是一位卓越的软件系统工程师、架构师或研究人员**必备的素养**。发起软件系统优化方面的课程设置和教学是解决我国计算机系统方面“卡脖子”问题人才培养的有效措施。

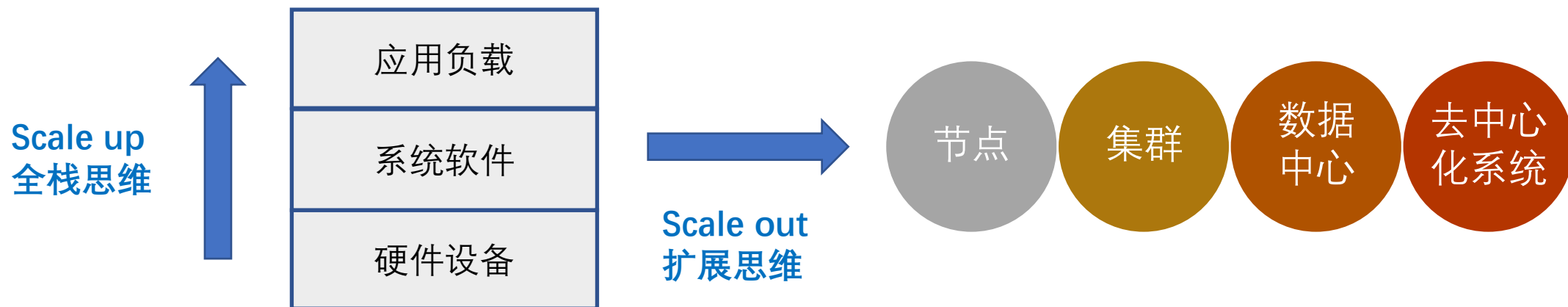
Table 1. Speedups from performance engineering a program that multiplies two 4096-by-4096 matrices. Each version represents a successive refinement of the original Python code. “Running time” is the running time of the version. “GFLOPS” is the billions of 64-bit floating-point operations per second that the version executes. “Absolute speedup” is time relative to Python, and “relative speedup,” which we show with an additional digit of precision, is time relative to the preceding line. “Fraction of peak” is GFLOPS relative to the computer’s peak 835 GFLOPS. See Methods for more details.

Version	Implementation	Running time (s)	GFLOPS	Absolute speedup	Relative speedup	Fraction of peak (%)
1	Python	25,552.48	0.005	1	—	0.00
2	Java	2,372.68	0.058	11	10.8	0.01
3	C	542.67	0.253	47	4.4	0.03
4	Parallel loops	69.80	1.969	366	7.8	0.24
5	Parallel divide and conquer	3.80	36.180	6,727	18.4	4.33
6	plus vectorization	1.10	124.914	23,224	3.5	14.96
7	plus AVX intrinsics	0.41	337.812	62,806	2.7	40.45

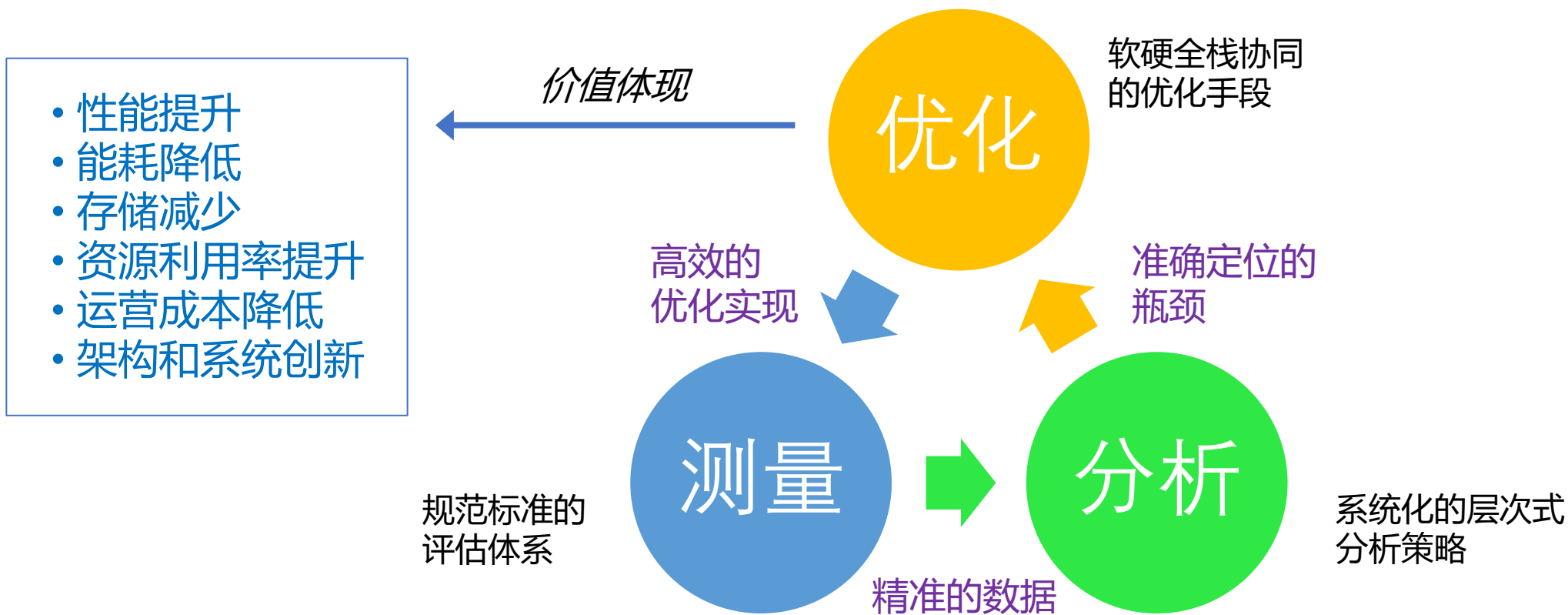
[C. E. Leiserson et al., There's plenty of room at the Top: What will drive computer performance after Moore's law? Science 368, eaam9744 (2020)]

培养“系统观”：从单点到全局的系统思维

2023 CCF CHINASOFT
中国软件大会



数据驱动的系统优化方法论



课程设置

- 专业选修课
- 面向大三、大四
- 2021年秋开设
- 2023年立项上海高校
市级重点课程

周	理论课		实践课	
	模块	主题	上机作业 (2周/个)	实践项目 (4周/个)
1	绪论	课程介绍、矩阵乘法优化案例	A1 初试环境和工具	
2	性能工程基础	性能测量		P1 Matrix Multiplication Autotuner
3		配置优化	1. A2 SPECjvm2008基准测试 2. A1 提交	
4		基准评测		
5		性能评价	1. A2 提交 2. A1-QA & Check	
6		编译优化	源程序级别的常见优化方法	A2-QA & Check
7	编译器概述		A3 GCC与Clang /LLVM优化比较	1. P1-QA & Check 2. P2 交叉编译与跨平台应用仿真
8	目标指令集架构及汇编语言			
9	C程序的汇编代码生成		A3 提交	
10	编译器的优化能力		1. A3-QA & Check 2. A4 Vectorization	
11	程序插桩及优化机会识别			P2 提交
12	计算机体系结构优化	计算机体系结构概论	A4 提交	1. P2-QA & Check 2. P3 Profiling Serial Merge Sort
13		多核异构编程	1. A4-QA & Check 2. A5 oneAPI异构编程	
14		高速缓存及相关优化		
15		微体系结构性能分析方法	A5 提交	
16	前沿研究和应用	数据中心和云端服务优化	A5-QA & Check	P3提交
17		机器学习框架优化		P3-QA & Check

感想2：加强对汇编代码的理解，知其所以然

example1.c

```
#include <stdint.h>
#include <stdlib.h>
#include <math.h>

#define SIZE (1L << 16)

void test(uint8_t * a, uint8_t * b) {
    uint64_t i;

    for (i = 0; i < SIZE; i++) {
        a[i] += b[i];
    }
}
```

感想2：加强对汇编代码的理解，知其所以然

example1.c

```
#include <stdint.h>
#include <stdlib.h>
#include <math.h>

#define SIZE (1L << 16)

void test(uint8_t * a, uint8_t * b) {
    uint64_t i;

    for (i = 0; i < SIZE; i++) {
        a[i] += b[i];
    }
}
```

```
$clang -Wall -g -std=gnu99
-O3 -Rpass=loop-vectorize
-Rpass-missed=loop-vectorize
-S -c example1.c
```

启用循环向量化选项，生成汇编代码

example1.s

```
#DEBUG_VALUE: test:b <- $rsi
#DEBUG_VALUE: test:a <- $rdi
.loc 1 0 3 # example1.c:0:3
xorl %eax, %eax
.Ltmp7:
.p2align 4, 0x90
.LBB0_3: # =>This Inner Loop Header: Depth=1
#DEBUG_VALUE: test:i <- 0
#DEBUG_VALUE: test:b <- $rsi
#DEBUG_VALUE: test:a <- $rdi
.loc 1 13 13 is_stmt 1 # example1.c:13:13
movdqu (%rsi,%rax), %xmm0
movdqu 16(%rsi,%rax), %xmm1
.loc 1 13 10 is_stmt 0 # example1.c:13:10
movdqu (%rdi,%rax), %xmm2
paddb %xmm0, %xmm2
movdqu 16(%rdi,%rax), %xmm0
paddb %xmm1, %xmm0
movdqu 32(%rdi,%rax), %xmm1
movdqu 48(%rdi,%rax), %xmm3
movdqu %xmm2, (%rdi,%rax)
movdqu %xmm0, 16(%rdi,%rax)
.loc 1 13 13 # example1.c:13:13
movdqu 32(%rsi,%rax), %xmm0
.loc 1 13 10 # example1.c:13:10
paddb %xmm1, %xmm0
.loc 1 13 13 # example1.c:13:13
movdqu 48(%rsi,%rax), %xmm1
.loc 1 13 10 # example1.c:13:10
paddb %xmm3, %xmm1
movdqu %xmm0, 32(%rdi,%rax)
movdqu %xmm1, 48(%rdi,%rax)
.Ltmp8:
.loc 1 12 26 is_stmt 1 # example1.c:12:26
addq $64, %rax
cmpq $65536, %rax # imm = 0x10000
jne .LBB0_3
```

感想2：加强对汇编代码的理解，知其所以然

example1.c

```
#include <stdint.h>
#include <stdlib.h>
#include <math.h>

#define SIZE (1L << 16)

void test(uint8_t * a, uint8_t * b) {
    uint64_t i;

    for (i = 0; i < SIZE; i++) {
        a[i] += b[i];
    }
}
```

```
$clang -Wall -g -std=gnu99
-O3 -Rpass=loop-vectorize
-Rpass-missed=loop-vectorize
-S -c example1.c
```

启用循环向量化选项，生成汇编代码

比较和分析不同汇编代码的差异

example1.s

```
#DEBUG_VALUE: test:b <- $rsi
#DEBUG_VALUE: test:a <- $rdi
.loc 1 0 3 # example1.c:0:3
xorl %eax, %eax
.Ltmp7:
.p2align 4, 0x90
.LBB0_3: # =>This Inner Loop
#DEBUG_VALUE: test:i <- 0
#DEBUG_VALUE: test:b <- $rsi
#DEBUG_VALUE: test:a <- $rdi
.loc 1 13 13 is_stmt 1 # example1.c:13:13
movdqu (%rsi,%rax), %xmm0
movdqu 16(%rsi,%rax), %xmm1
.loc 1 13 10 is_stmt 0 # example1.c:13:10
movdqu (%rdi,%rax), %xmm2
paddb %xmm0, %xmm2
movdqu 16(%rdi,%rax), %xmm0
paddb %xmm1, %xmm0
movdqu 32(%rdi,%rax), %xmm1
movdqu 48(%rdi,%rax), %xmm3
movdqu %xmm2, (%rdi,%rax)
movdqu %xmm0, 16(%rdi,%rax)
.loc 1 13 13 # example1.c:13:13
movdqu 32(%rsi,%rax), %xmm0
.loc 1 13 10 # example1.c:13:10
paddb %xmm1, %xmm0
.loc 1 13 13 # example1.c:13:13
movdqu 48(%rsi,%rax), %xmm1
.loc 1 13 10 # example1.c:13:10
paddb %xmm3, %xmm1
movdqu %xmm0, 32(%rdi,%rax)
movdqu %xmm1, 48(%rdi,%rax)
.Ltmp8:
.loc 1 12 26 is_stmt 1 # example1.c:12:26
addq $64, %rax
cmpq $65536, %rax # imm = 0x10000
jne .LBB0_3
```

example1-opt.s

```
.LBB0_2: # %vector.body.preheader
#DEBUG_VALUE: test:b <- %rsi
#DEBUG_VALUE: test:a <- %rdi
.loc 1 0 3 # example1.c:0:3
movq $-65536, %rax # imm = 0xFFFF0000
.p2align 4, 0x90
.LBB0_3: # %vector.body
# =>This Inner Loop Header: Depth=1
#DEBUG_VALUE: test:b <- %rsi
#DEBUG_VALUE: test:a <- %rdi
.Ltmp3:
.loc 1 13 13 is_stmt 1 # example1.c:13:13
movdqu 65536(%rsi,%rax), %xmm0
movdqu 65552(%rsi,%rax), %xmm1
.loc 1 13 10 is_stmt 0 # example1.c:13:10
movdqu 65536(%rdi,%rax), %xmm2
paddb %xmm0, %xmm2
movdqu 65552(%rdi,%rax), %xmm0
movdqu 65568(%rdi,%rax), %xmm3
movdqu 65584(%rdi,%rax), %xmm4
movdqu %xmm2, 65536(%rdi,%rax)
paddb %xmm1, %xmm0
movdqu %xmm0, 65552(%rdi,%rax)
.loc 1 13 13 # example1.c:13:13
movdqu 65568(%rsi,%rax), %xmm0
.loc 1 13 10 # example1.c:13:10
paddb %xmm3, %xmm0
.loc 1 13 13 # example1.c:13:13
movdqu 65584(%rsi,%rax), %xmm1
.loc 1 13 10 # example1.c:13:10
movdqu %xmm0, 65568(%rdi,%rax)
paddb %xmm4, %xmm1
movdqu %xmm1, 65584(%rdi,%rax)
.Ltmp4:
.loc 1 12 26 is_stmt 1 # example1.c:12:26
addq $64, %rax
jne .LBB0_3
```


感想2: 加强对汇编代码的理解, 知其所以然



```
example1.c
#include <stdint.h>
#include <stdlib.h>
#include <math.h>

#define SIZE (1L << 16)

void test(uint8_t * a, uint8_t * b) {
    uint64_t i;

    for (i = 0; i < SIZE; i++) {
        a[i] += b[i];
    }
}
```

```
$clang -Wall -g -std=gnu99
-O3 -Rpass=loop-vectorize
-Rpass-missed=loop-vectorize
-S -c example1.c
```

启用循环向量化选项, 生成汇编代码

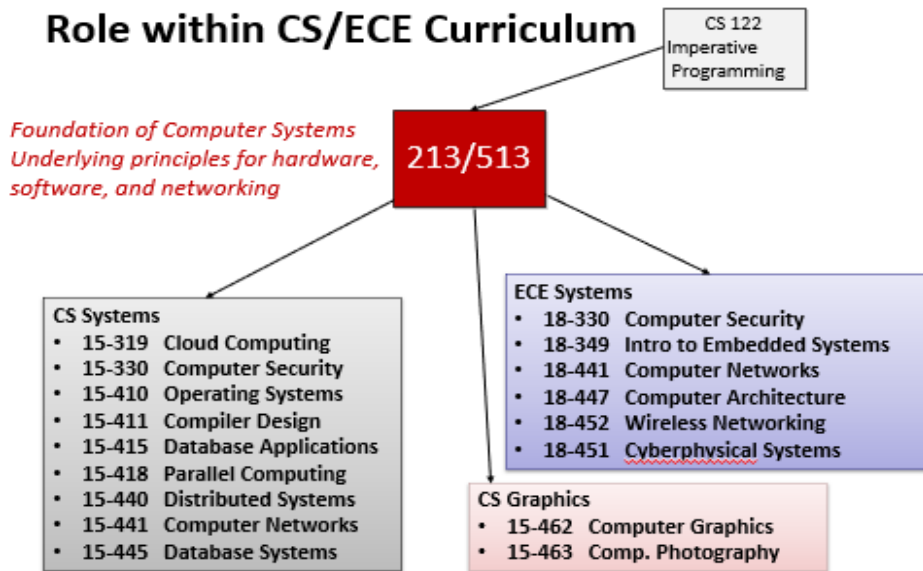
比较和分析不同汇编代码的差异

```
example1.s
#DEBUG_VALUE: test:b <- $rsi
#DEBUG_VALUE: test:a <- $rdi
.loc 1 0 3 # example1.c:0:3
xorl %eax, %eax
.Ltmp7:
.p2align 4, 0x90
.LBB0_3: # =>This Inner Loop
#DEBUG_VALUE: test:i <- 0
#DEBUG_VALUE: test:b <- $rsi
#DEBUG_VALUE: test:a <- $rdi
.loc 1 13 13 is_stmt 1 # example1.c:13:13
movdqu (%rsi,%rax), %xmm0
movdqu 16(%rsi,%rax), %xmm1
.loc 1 13 10 is_stmt 0 # example1.c:13:10
movdqu (%rdi,%rax), %xmm2
paddb %xmm0, %xmm2
movdqu 16(%rdi,%rax), %xmm0
paddb %xmm1, %xmm0
movdqu 32(%rdi,%rax), %xmm1
movdqu 48(%rdi,%rax), %xmm3
movdqu %xmm2, (%rdi,%rax)
movdqu %xmm0, 16(%rdi,%rax)
.loc 1 13 13 # example1.c:13:13
movdqu 32(%rsi,%rax), %xmm0
.loc 1 13 10 # example1.c:13:10
paddb %xmm1, %xmm0
.loc 1 13 13 # example1.c:13:13
movdqu 48(%rsi,%rax), %xmm1
.loc 1 13 10 # example1.c:13:10
paddb %xmm3, %xmm1
movdqu %xmm0, 32(%rdi,%rax)
movdqu %xmm1, 48(%rdi,%rax)
.Ltmp8:
.loc 1 12 26 is_stmt 1 # example1.c:12:26
addq $64, %rax
cmpq $65536, %rax # imm = 0x10000
jne .LBB0_3
```

```
example1-opt.s
.LBB0_2: # %vector.body.preheader
#DEBUG_VALUE: test:b <- %rsi
#DEBUG_VALUE: test:a <- %rdi
.loc 1 0 3 # example1.c:0:3
xorl %eax, %eax
movq $-65536, %rax # imm = 0xFFFF0000
.p2align 4, 0x90
.LBB0_3: # %vector.body
# =>This Inner Loop Header: Depth=1
#DEBUG_VALUE: test:b <- %rsi
#DEBUG_VALUE: test:a <- %rdi
.Ltmp3:
.loc 1 13 13 is_stmt 1 # example1.c:13:13
movdqu 65536(%rsi,%rax), %xmm0
movdqu 65552(%rsi,%rax), %xmm1
.loc 1 13 10 is_stmt 0 # example1.c:13:10
movdqu 65536(%rdi,%rax), %xmm2
paddb %xmm0, %xmm2
movdqu 65552(%rdi,%rax), %xmm0
movdqu 65568(%rdi,%rax), %xmm3
movdqu 65584(%rdi,%rax), %xmm4
movdqu %xmm2, 65536(%rdi,%rax)
paddb %xmm1, %xmm0
movdqu %xmm0, 65552(%rdi,%rax)
.loc 1 13 13 # example1.c:13:13
movdqu 65568(%rsi,%rax), %xmm0
.loc 1 13 10 # example1.c:13:10
paddb %xmm3, %xmm0
.loc 1 13 13 # example1.c:13:13
movdqu 65584(%rsi,%rax), %xmm1
.loc 1 13 10 # example1.c:13:10
movdqu %xmm0, 65568(%rdi,%rax)
paddb %xmm4, %xmm1
movdqu %xmm1, 65584(%rdi,%rax)
.Ltmp4:
.loc 1 12 26 is_stmt 1 # example1.c:12:26
addq $64, %rax
jne .LBB0_3
```

- 教学I: 软件系统优化
- **教学II: 计算机系统**
- 实践I: 跨平台的硬件性能计数器自适应分组复用 [TACO' 23]
- 实践II: 不同编译器优化能力的分析与识别 [CC' 23]

- 从程序员的角度来看计算机系统，贯穿整个计算机科学与工程的基础课程
- 面向计算机科学拔尖基地大一学生



[CMU 15-213 Intro to Computer Systems]



学期	必修课
大一（上）	线性代数 计算机数学分析（上） 程序设计
大一（下）	计算机数学分析（下） 数据结构 计算机系统 离散数学与算法
大一暑假	应用编程实践 设计思维
大二（上）	操作系统 计算机网络 概率与统计
大二（下）	数据管理系统 编译原理 AI 基础
大二暑假	云计算系统 软件工程实践
大四（下）	毕业设计

Chapter	Topic	Course				
		ORG	ORG+	ICS	ICS+	SP
1	Tour of systems	•	•	•	•	•
2	Data representation	•	•	•	•	⊙ ^(d)
3	Machine language	•	•	•	•	•
4	Processor architecture	•	•	•	•	•
5	Code optimization		•	•	•	•
6	Memory hierarchy	⊙ ^(a)	•	•	•	⊙ ^(a)
7	Linking			⊙ ^(c)	⊙ ^(c)	•
8	Exceptional control flow			•	•	•
9	Virtual memory	⊙ ^(b)	•	•	•	•
10	System-level I/O				•	•
11	Network programming				•	•
12	Concurrent programming				•	•

Figure 2 Five systems courses based on the CS:APP book. ICS+ is the 15-213 course from Carnegie Mellon. Notes: The ⊙ symbol denotes partial coverage of a chapter, as follows: (a) hardware only; (b) no dynamic storage allocation; (c) no dynamic linking; (d) no floating point.

[Bryant and O'Hallaron, Computer Systems: A Programmer's Perspective, 3rd edition (CS:APP3e), Pearson, 2016]



周	理论课主题	教材章节	实践课		Labs
1	概述	1	Bootcamp 1: Linux, Command Line, Git	shell、脚本、vim、命令行、调试	
2	信息的表示和处理	2	Bootcamp 2: Debugging & GDB	git, 性能分析, 数据整理	
3			Bootcamp 3: GCC & Build Automation		
4	程序的机器级表示	3	L1布置 (3月17日)	元编程	L1
5			L1提交, L2布置 (3月31日)	安全和密码学	L2
6					
7			L2提交 (4月14日)		
8	指令集架构	4.1	L1&L2 QA, L3布置 (4月21日)		L3
9	存储器层次结构	6			
10	程序性能优化	5	L3提交 (5月5日)		L4
11			L3 QA, L4布置 (5月12日)		
12	虚拟内存	9			
13			L4提交 (5月26日)		
14	链接	7	L4 QA, L5布置 (6月2日)		L5
15					
16	异常控制流	8	L5提交 (6月16日)		
17			L5 QA (6月23日)		
18	期末考试				

感想3：不纯粹依赖书本，结合系统本身去教和学

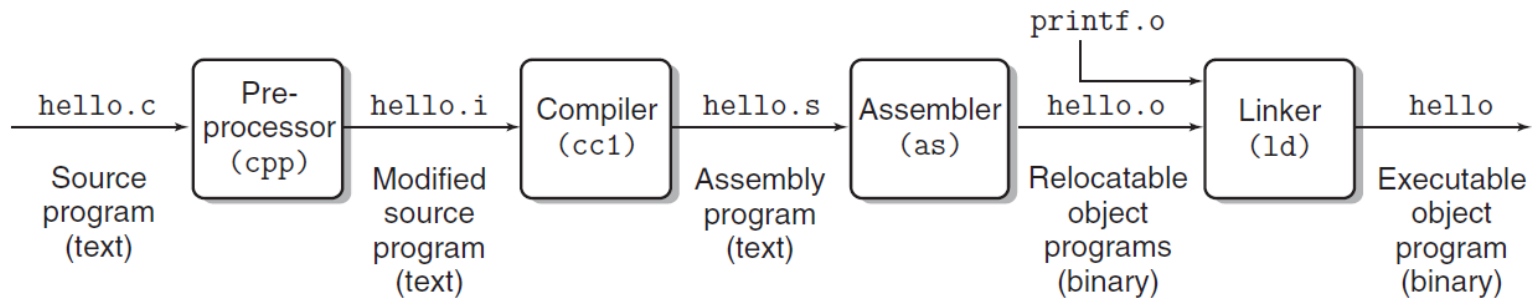


Figure 1.3 The compilation system.

[Bryant and O'Hallaron, Computer Systems: A Programmer's Perspective, 3rd edition (CS:APP3e), Pearson, 2016]

感想3：不纯粹依赖书本，结合系统本身去教和学

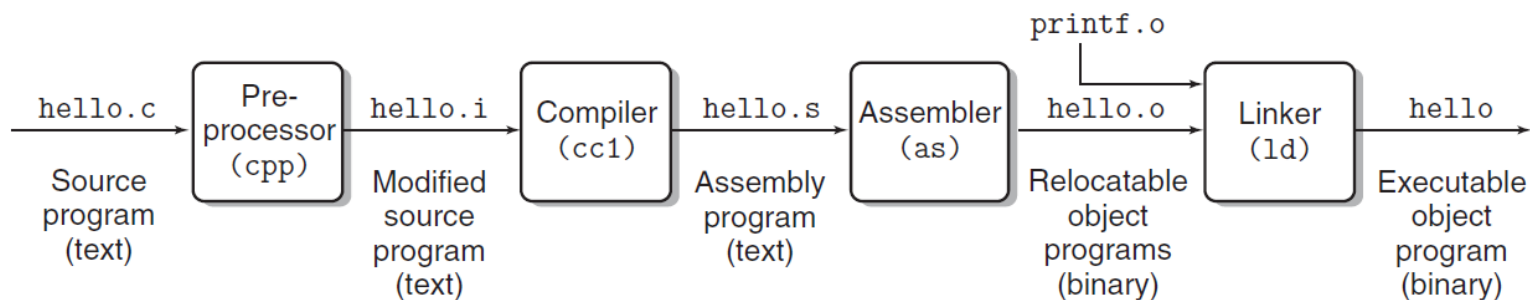
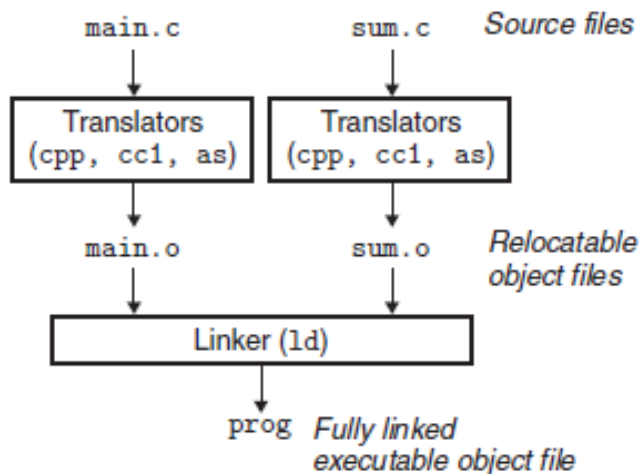


Figure 1.3 The compilation system.

Figure 7.2

Static linking. The linker combines relocatable object files to form an executable object file prog.



```
linux> gcc -Og -o prog main.c sum.c  
cpp [other arguments] main.c /tmp/main.i  
cc1 /tmp/main.i -Og [other arguments] -o /tmp/main.s  
as [other arguments] -o /tmp/main.o /tmp/main.s  
ld -o prog [system object files and args] /tmp/main.o /tmp/sum.o
```

[Bryant and O'Hallaron, Computer Systems: A Programmer's Perspective, 3rd edition (CS:APP3e), Pearson, 2016]

感想3：不纯粹依赖书本，结合系统本身去教和学

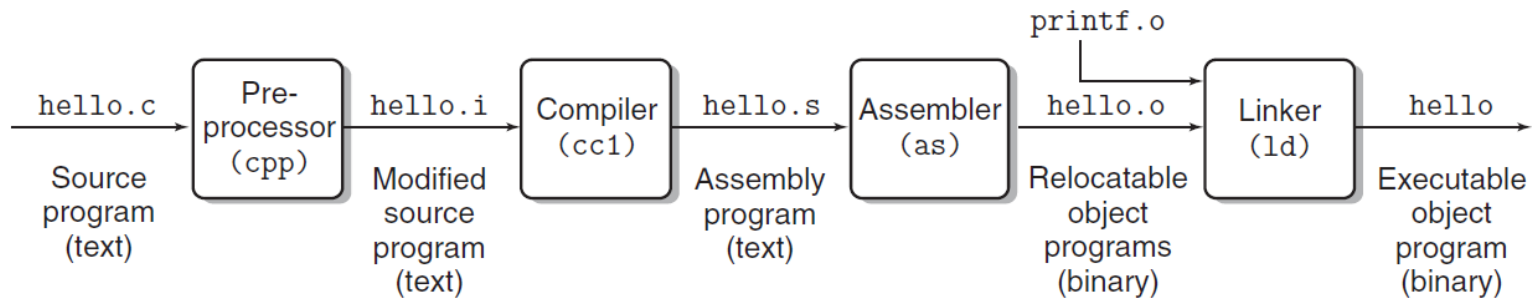
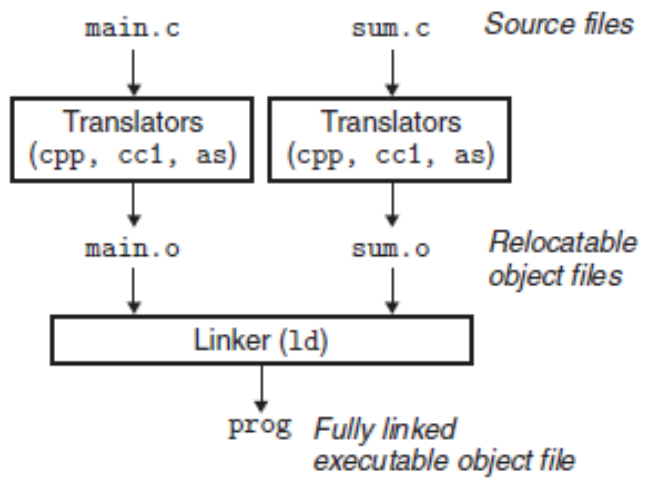


Figure 1.3 The compilation system.

Figure 7.2 Static linking. The linker combines relocatable object files to form an executable object file prog.



```
linux> gcc -Og -o prog main.c sum.c  
cpp [other arguments] main.c /tmp/main.i  
cc1 /tmp/main.i -Og [other arguments] -o /tmp/main.s  
as [other arguments] -o /tmp/main.o /tmp/main.s  
ld -o prog [system object files and args] /tmp/main.o /tmp/sum.o
```

System object files and other arguments?

[Bryant and O'Hallaron, Computer Systems: A Programmer's Perspective, 3rd edition (CS:APP3e), Pearson, 2016]

- 教学I: 软件系统优化
- 教学II: 计算机系统
- **实践I: 跨平台的硬件性能计数器自适应分组复用 [TACO' 23]**
- 实践II: 不同编译器优化能力的分析与识别 [CC' 23]

RESEARCH-ARTICLE FREE ACCESS



Efficient Cross-platform Multiplexing of Hardware Performance
Counters via Adaptive Grouping
Just Accepted

Authors: [Tong-yu Liu](#), [Jianmei Guo](#), [Bo Huang](#) [Authors Info & Claims](#)

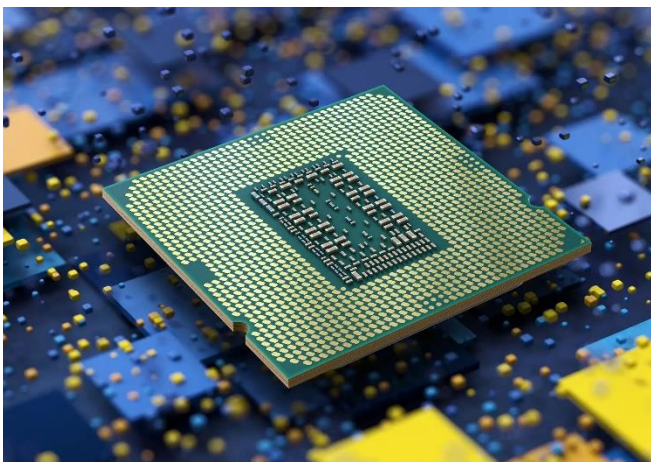
ACM Transactions on Architecture and Code Optimization • Accepted on September 2023 • <https://doi.org/10.1145/3629525>

开源项目: <https://jihulab.com/solecnu/hperf>

Software
performance



Hardware
performance



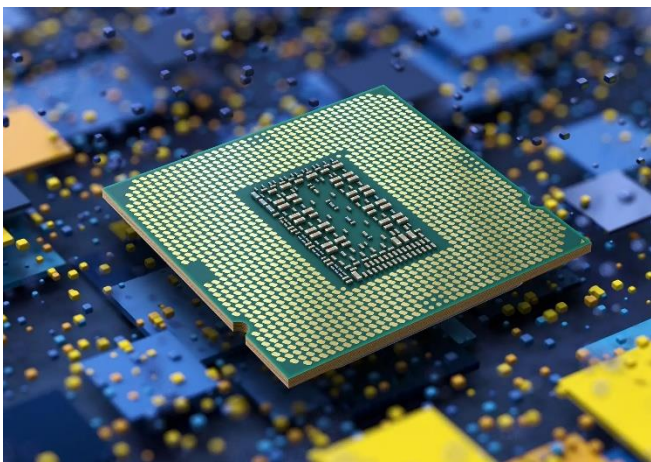
Software
performance



Performance
metrics



Hardware
performance



Hardware Performance
Counters

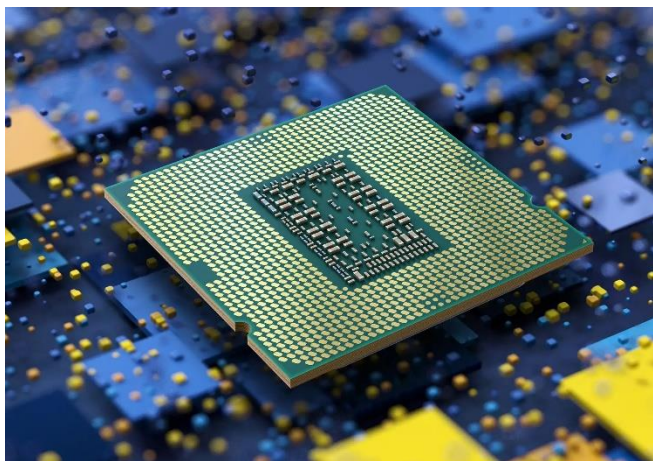
Software
performance



**Many (e.g., hundreds)
Performance metrics**



Hardware
performance



**A few (e.g., 7 on Intel CLX)
Hardware Performance Counters**

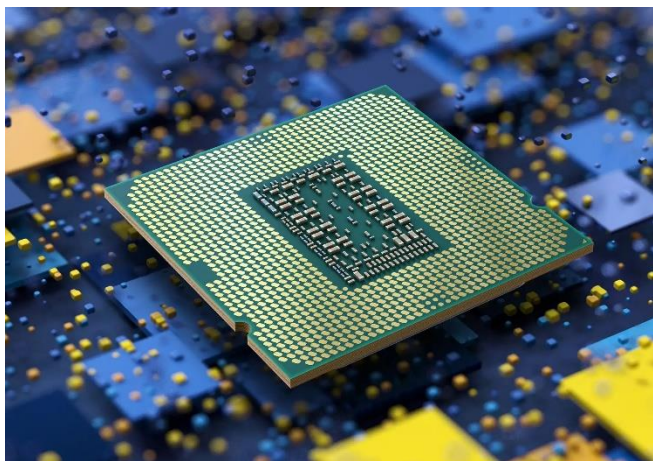
Software
performance



Many (e.g., hundreds)
Performance metrics



Hardware
performance



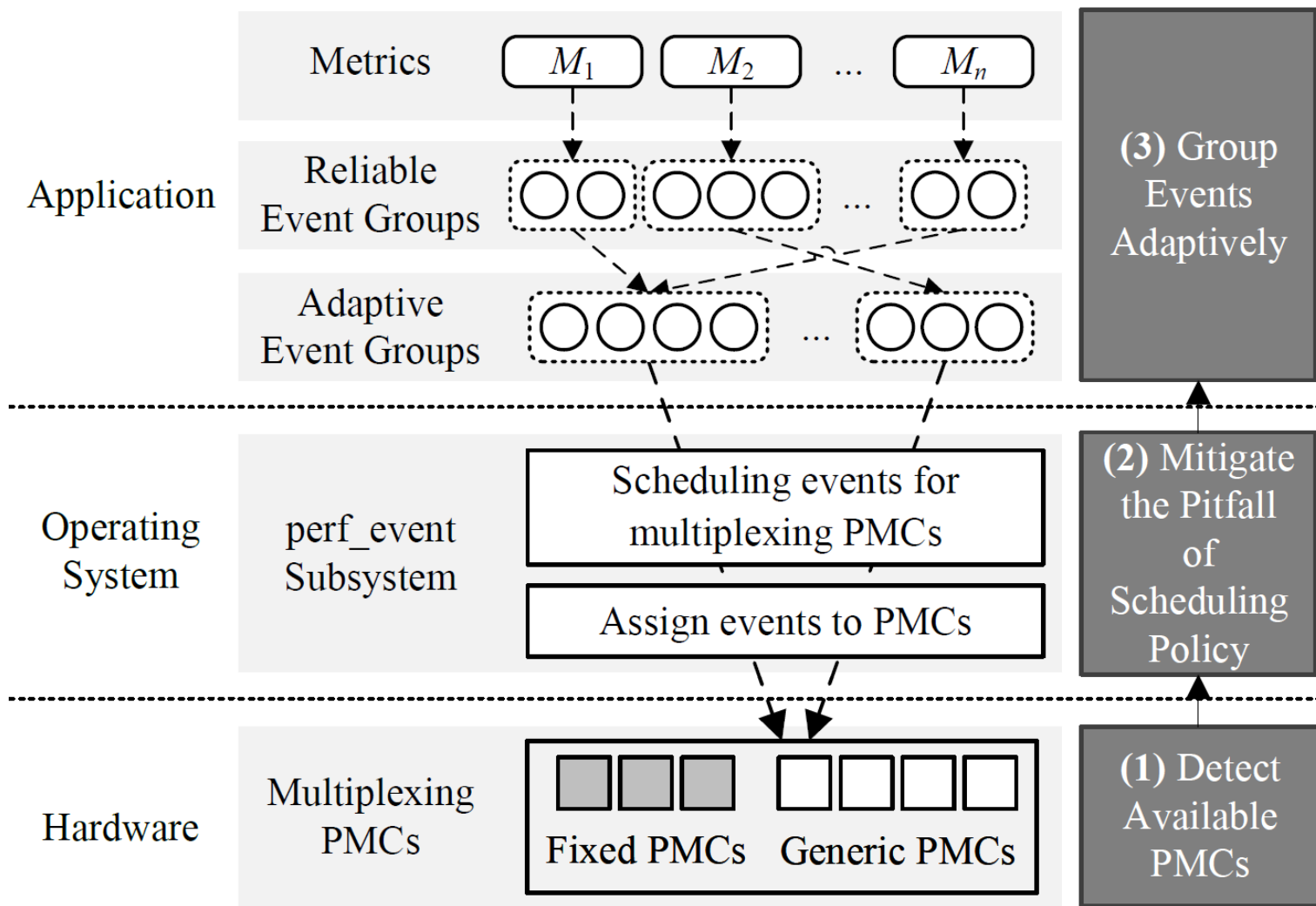
A few (e.g., 7 on Intel CLX)
Hardware Performance Counters
on different platforms

intel

AMD

AMPERE

Kunpeng



Key concerns:

- Inefficient grouping
- Event scheduling pitfall in Linux perf_event
- Unawareness of available counters

Table 7. Results of the Detection of Available PMCs for Mainstream Processors [9, 29]

Machine	ISA	Processor	From detection		From documentation	
			# Fixed PMCs	# Generic PMCs	# Fixed PMCs	# Generic PMCs
A	x86-64 ¹	Intel Xeon Gold 5218R (CascadeLake)	3	4	3	4
B		Intel Xeon Platinum 8352Y (IceLake)	4	8	4	8
C	AArch64	Hisilicon Kunpeng 920	1	12	1	6
D		Ampere Altra	1	6	1	6

¹ NMI watchdogs are disabled.

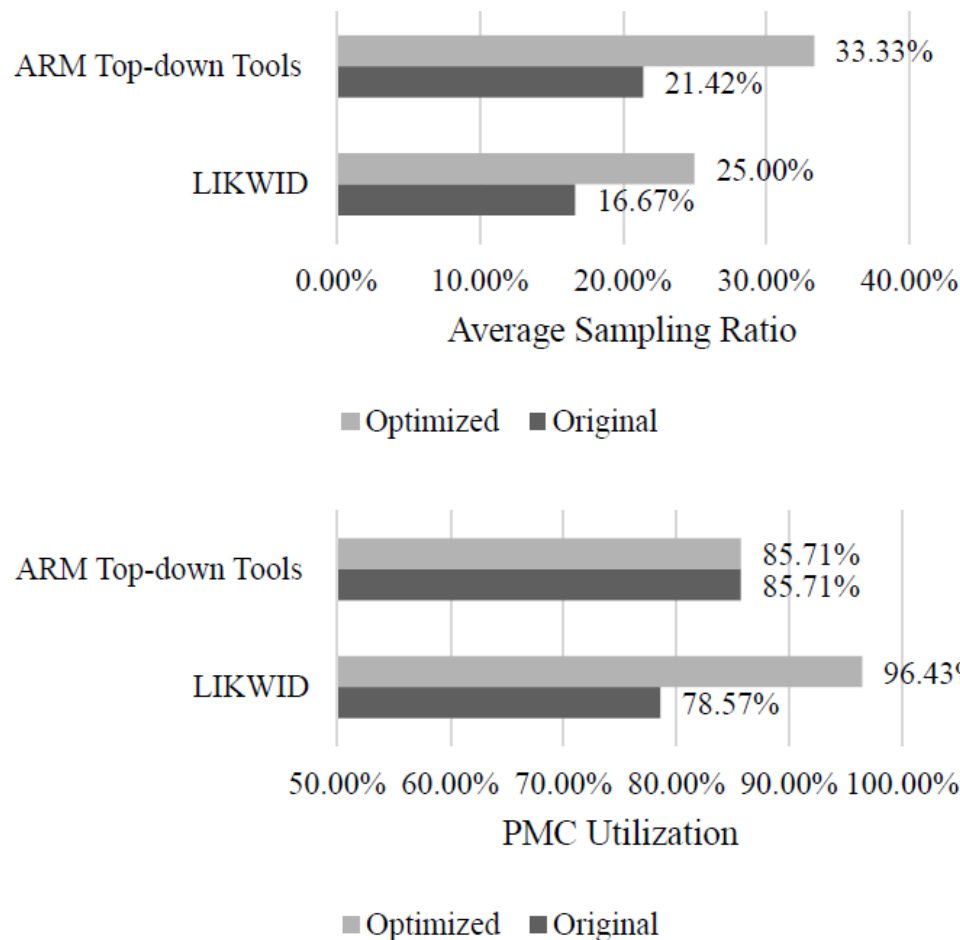


Fig. 9. Comparison of efficiency between before and after optimization.

- 教学I: 软件系统优化
- 教学II: 计算机系统
- 实践I: 跨平台的硬件性能计数器自适应分组复用 [TACO' 23]
- **实践II: 不同编译器优化能力的分析与识别 [CC' 23]**

ACM SIGPLAN 2023 International
Conference on Compiler Construction

February 25-26, 2023, Montréal, Québec, Canada.

A Hotspot-Driven Semi-automated Competitive Analysis Framework for Identifying Compiler Key Optimizations

Wenlong Mu East China Normal University, Yilei Zhang East China Normal University, Bo Huang East China Normal University, Jianmei Guo East China Normal University, Shiqiang Cui Hangzhou Hongjun
Microelectronics Technology

开源项目: <https://jihulab.com/solecnu/hotspotdrivenframework>

Open source compilers



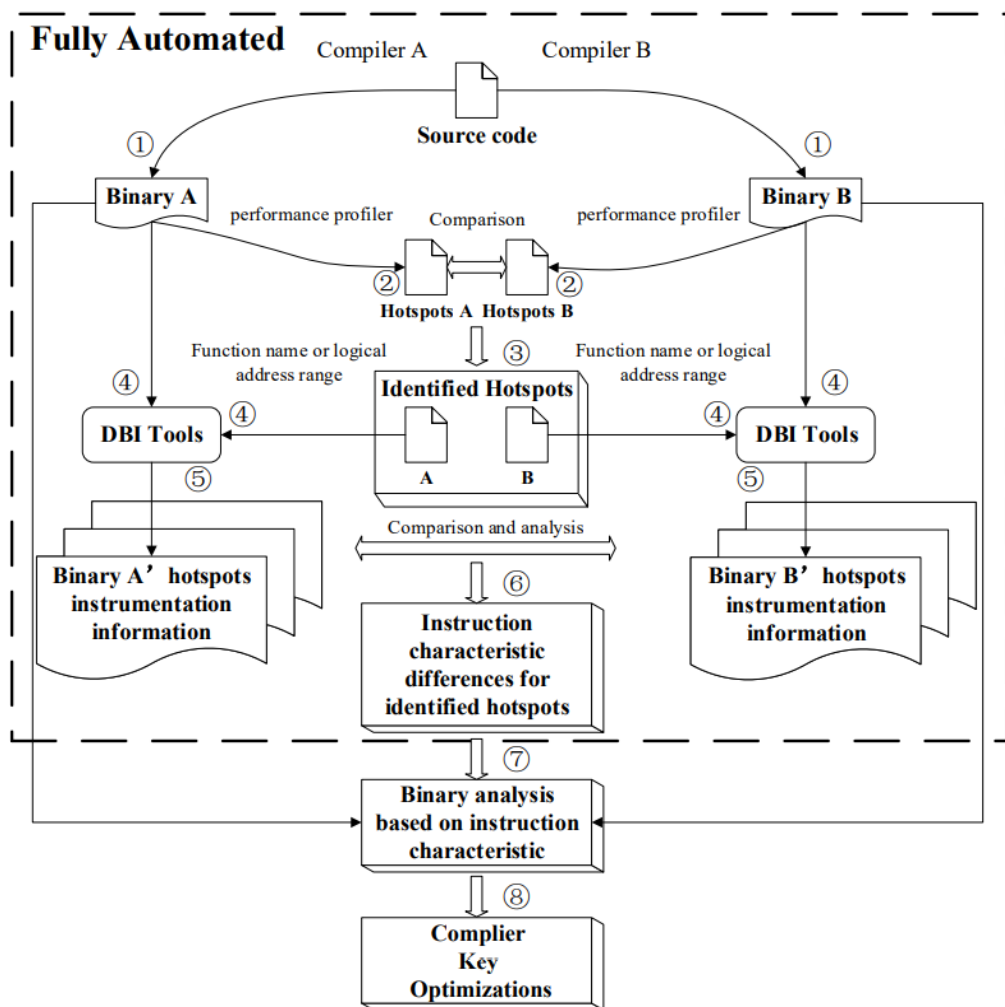
Proprietary compilers

华为 毕昇编译器

Intel® oneAPI DPC++/C++ Compiler
A Standards-Based, Cross-architecture Compiler

AMD Optimizing C/C++ and Fortran Compilers (AOCC)

How to “learn” from other compilers?



Major steps(automation part):

- **Hotspot detection**: generate the top N hotspots with *perf* for each compiler-generated binary, reformatting the hotspot representation as required by our *DynamoRIO* tools
- **Hotspot selection**: select the "identified hotspots" according to the predefined rules respectively for the two hotspot lists
- **Instrumentation only for "identified hotspots"**: build *DynamoRIO* tools so that only "identified hotspots" are instrumented, and the instrumentation for each identified hotspot can be done in parallel
- **Instruction characteristic comparison**: compare the instruction characteristic difference for associated "identified hotspots" respectively in two compiler-generated binaries

结果 (gcc vs. icc/bisheng)

Top 10 hotspot lists of 648.exchange2_s using BiSheng and GCC

Rank	BiSheng			GCC		
	relative time(%)	absolute time(sec)	function name	relative time(%)	absolute time(sec)	function name
1	57.32	14.33	<i>digits_2.7</i>	82.38	32.05	<i>MOD_digits_2</i>
2	19.38	4.85	<i>digits_2.4</i>	7.30	2.84	<i>gfortran_mminloc</i>
3	18.30	4.58	<i>logic_new_solver</i>	4.15	1.61	<i>specific.4</i>
4	1.46	0.37	<i>free</i>	2.31	0.90	<i>logic_MOD_new_solver</i>
5	0.80	0.20	<i>malloc</i>	1.08	0.42	<i>hidden_triplets.0</i>
6	0.43	0.11	<i>covered</i>	0.78	0.30	<i>free</i>
7	0.32	0.08	<i>brute</i>	0.55	0.21	<i>naked_triplets.1</i>
8	0.17	0.04	<i>f90_dealloc</i>	0.48	0.19	<i>hidden_pairs.2</i>
9	0.15	0.04	<i>f90_alloc</i>	0.25	0.10	<i>MOD_brute</i>
10	0.14	0.04	<i>f90_set_intrin</i>	0.25	0.10	<i>malloc</i>

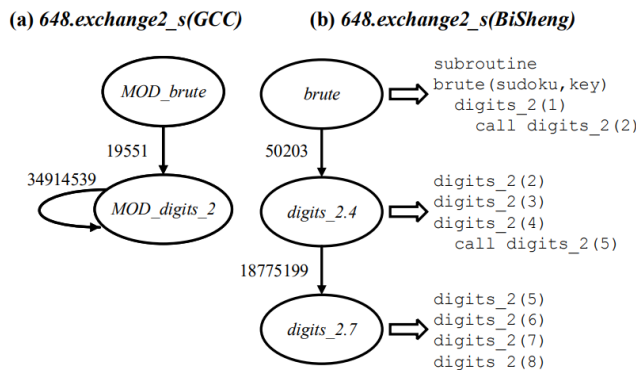
Optimization results on a GCC-based production compiler

Benchmark	before		after		improvement (score)
	runtime(sec)	score	runtime(sec)	score	
<i>548.exchange2_r</i>	527	636	306	1100	72.96%
<i>505.mcf_r</i>	1350	153	776	234	52.94%
<i>525.x264_r</i>	344	651	271	828	27.19%

*Setup: Huawei Kunpeng 920 7261K (2 sockets, 128 cores in total); Operating System: CentOS Linux release 7.9.2009; SPEC2017 input data size: reference.

Instruction characterization

Category	<i>digit_2.4</i>	<i>digit_2.7</i>	<i>MOD_digit_2</i>
Branch	3593142992	11609217450	29321533296
Indirect	50203	18775199	34934090
Jump	0	0	0
Call	0	0	0
Ret	50203	18775199	34934090
Cond	3361685141	10733730071	27698651020
Uncond Direct	231407648	856712180	1587948186
Operation	15199221930	41803072569	116664503804
Binary Arith	14686454611	39759170962	102813533806
Logical	498710685	1733736947	12559811188
Shift	14056634	310164660	1291158810
Data Transfer	32692266	214161627	1636029315
Load/Store	11911031090	37507748969	81757176763
Others	534241804	1514087575	2581871188
Vectorization	5426852903	10429222578	268662027



Function inlining

Optimization guidance for GCC (AArch64)

Benchmark	Optimization suggestions for GCC
<i>605.mcf_s</i>	(1) Optimize the capacity of inlining callback functions to eliminate indirect subroutine calls. (2) Optimize memory layout of the data structure, including structure peeling and unused field elimination.
<i>648.exchange2_s</i>	(1) Improve the optimization of inlining the non-tail recursive function calls. (2) Implement function specialization. (3) Optimize the capacity of automatic vectorization. (4) Inline the built-in functions for Fortran code.
<i>625.x264_s</i>	(1) Optimize the capacity of automatic vectorization. (2) Optimize the decisions of whether to inline a function.

- 教学I：软件系统优化
 - 教学II：计算机系统
 - 实践I：跨平台的硬件性能计数器自适应分组复用 [TACO' 23]
 - 实践II：不同编译器优化能力的分析与识别 [CC' 23]
-
- 加强动手能力培养，开展即时反馈的实践课
 - 加强对汇编代码的理解，知其所以然
 - 不单纯依赖书本，结合系统本身去教和学
 - 结合产业需求，打破性能瓶颈，做实在的研究



2023 CCF CHINASOFT
中国软件大会

感谢观看

